

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Praveen Parvathala

Title: FUNCTIONAL RANDOM INSTRUCTION TESTING (FRIT) METHOD FOR COMPLEX DEVICES SUCH AS MICROPROCESSORS

Docket No.: 80107.051US1

Filed: July 31, 2001

Examiner: Unknown



Serial No.: 09/917661

Due Date: N/A

Group Art Unit: 2184

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- ☒ Revocation and Power of Attorney Statement under 37 CFR § 3.73(b) (2 pgs.).
- ☒ Status Inquiry (1 pg.).

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P.O. Box 52050
Minneapolis, MN 55402
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By: Dana B. LeMoine
Atty: Dana LeMoine
Reg. No. 40,062

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- ☒ Copy of Revocation and Power of Attorney, Status Inquiry filed August 27, 2003 (5 pgs.).

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Emily Bates
Name

Emily Bates
Signature

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